

**FACULTY OF TECHNOLOGY  
UNIVERSITY OF DELHI**

Exam Centre: NSIT Dwarka

**M.TECH. DATE SHEET FOR 2015-16**

**M.TECH.-SIGNAL PROCESSING /INFORMATION SYSTEM /PROCESS CONTROL**

**End Semester Examination**

**Time: 10:00am to 01:00 pm**

**M.Tech. Full-Time & Part-Time, Ist, IIIrd semester**

DATE	DAY	PAPER CODE	SUBJECT
18/11/15	Wednesday	TH1SP 501	Communication Theory
		TH1 IS 501	Conceptual Modeling
		TH1 PC501	Modern Control Theory
20/11/15	Friday	TH2SP 502	Digital Systems Design
		TH2 IS 502	Object Orientation
		TH2PC 502	Power Electronics
23/11/15	Monday	TH3SP 503	Signal Analysis
		TH3 IS 503	Software Engineering
		TH3PC503	Process Instrumentation
26/11/15	Thursday	TH4SP 504	Analog MOS IC's for Signal Processing (Elect-I)
		TH4IS 504	Object Oriented Conceptual Modeling (Elect-I)
		TH4PC 504	Random Process in Estimation & Control (Elect-I)
30/11/15	Monday	TH5 SP 505	Adaptive Signal Processing (Elect-II)
		TH5 IS 505	Emerging Trends (Elect-II)
		TH5PC 505	Intelligent Instrumentation (Elect-II)

  
Dean, Faculty of Technology  
University of Delhi

**Copy to:**

1. Controller of Examination D.U.
2. HOD ECE/COE/ICE.
3. Supdt. Of Th. Examination
4. Dr. Duru Arun Kumar (with request to place it on Institute website)
5. P.S. to Director
6. S.O. (FOT)
7. All Notice Board